FACULTY PROFILE

Name of the faculty: J. JAYA KEERTHANA

Department: ECE

College: Seshadri Rao Gudlavalleru Engineering College

E-Mail: jayakeerthana44@gmail.com

RESIDENTIAL ADDRESS: F2-9, JP Complex, Katuru
Road, Vuyyuru, Krishna Dt, A.P-521165

ACADEMIC QUALIFICATIONS: M.Tech.

ACADEMIC REWARDS/ ACHIEVEMENTS:

- ➢ M.Tech (Radar & Microwave Engineering) with an aggregate of 89% from Andhra University College of Engineering in 2021-2023.
- ➢ B.Tech (E.C.E) with an aggregate of 76% from Usha Rama College of Engineering & Technology, JNTUK University in 2017-2021.
- ➢ Intermediate with an aggregate of 95% from State Board of Intermediate in 2015-2017.
- ➢ SSC with an aggregate of 85% State Board of Secondary Education in 2014-2015.

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	SR Gudlavalleru Engineering College	Oct 13th 2023 to till date

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
-	-	1

SPECIALIZATION: RADAR & MICROWAVE ENGINEERING

PROFESSIONAL SKILLS:

- Python , C, C++
- HFSS, CST Studio Suite
- MATLAB, Multisim

RESEARCH PAPERS/BOOKS:

PAPERS PUBLISHED IN JOURNALS:

1. J. Jaya Keerthana, M. Anupama Latha, M. V. S. Kathik, M. Manikanta, M. Raghavendra Rao "**Design and Implementation of Compact Planar Array Antenna for Ultra-wide band Applications** ", International Research Journal of Engineering and Technology (IRJET) , Volume: 11 Issue: 04 , April 2024 .

PAPERS PUBLISHED IN CONFERENCES/WORKSHOPS: Nil

LIST OF WORKSHOPS ORGANIZED: Nil

CERTIFICATIONS/Training Programs Attended No: Nil

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

1. One week Refresher Course on "Transmission Lines and Waveguides" organized by department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 29 th Jan – 2 nd Feb 2024.

2. One week Refresher Course on "Microcontrollers and Embedded Systems" organized by department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 9 th Jan – 13th Jan 2024.

3. Five days Orientation Program on "Computer Networks", organized by department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 18 th June – 22 nd June 2024.

4. One Week STC on "Microstrip Patch Antenna Design Techniques and Tools" conducted by NITTTR, Chandigarh during 16th Sep - 20th Sep 2024.

CERTIFICATIONS/Training Programs Attended No:

NPTEL COURSE CERTIFICATIONS:

S.No	Subject	Month & Year	Grade
1.	Digital Electronics Circuits	Jan-Apr 2024	Successfully Completed
2.	Digital Circuits	Jul-Oct 2024	Elite

PAPER PRESENTATION IN SEMINARS: Nil

GUEST LECTURES DELIVERED: Nil

CONSULTANCY ACTIVITIES: Nil

GRANTS FETCHED: Nil

Subjects Handled:

1. Radar Engineering
2. Digital Logic & Computer Organization
3. Embedded System based IoT

Labs Handled:

1. Radio Frequency and Optical Communications Lab
2. Analog Circuits Lab
3. Semiconductor Devices and Circuits Lab
4. Digital Logic Design Lab
5. Electrical & Electronics Engineering Workshop lab

Extra Curricular Activities: Department level activities

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 18-03-2025   J. Jaya Keerthana

Place: Gudlavalleru   Signature